

IN THE SPECIFICATION

Please amend the "Cross-Reference to Related Applications" section on page 1 of the application as follows:

CROSS-REFERENCE TO RELATED APPLICATIONS

This application claims priority under 35 U.S.C. § 119(e) to United States Provisional Patent Application No. 60/189,768 entitled "Optimal Symbol Distribution for Intersymbol Interference Channels with a Block DFE Receiver" filed on March 16, 2000.

The present invention is related to those disclosed in the following United States Patent Applications:

1. Serial No. 09/627,237 [~~Docket No. 701074~~], filed concurrently herewith, entitled "SYSTEMS AND METHODS FOR PRECURSOR CANCELLATION OF INTERSYMBOL INTERFERENCE IN A RECEIVER";
2. Serial No. 09/627,191 [~~Docket No. 701075~~], filed concurrently herewith, entitled "SYSTEMS AND METHODS FOR OPTIMAL SYMBOL SPACING TO MINIMIZE INTERSYMBOL INTERFERENCE IN A RECEIVER"; and
3. Serial No. 09/627,453 [~~Docket No. 701076~~], filed concurrently herewith, entitled "DUAL EQUALIZER FOR USE IN A RECEIVER AND METHOD OF OPERATION".

The above applications are commonly assigned to the assignee of the present invention. The disclosures of these related patent applications are hereby incorporated by reference for all purposes as if fully set forth herein.